

**(54) COLOR DECODER CIRCUIT**

- (11) 58-186288 (A) (43) 31.10.1983 (19) JP  
 (21) Appl. No. 57-68293 (22) 23.4.1982  
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 (51) Int. Cl.<sup>3</sup> H04N9/497, G09G1/28

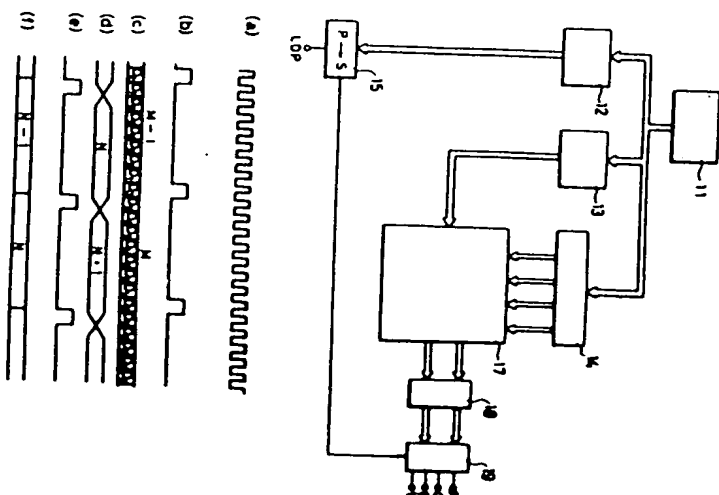
**PURPOSE:** To reduce the malfunction, by providing a margin for a decode time to generate color signal data, applying one time decoding of the color data during 8-bit series period of the pattern data and applying switching depending on the content of the pattern data further.

**CONSTITUTION:** Two sets of R, G, B color signal data are latched at a latch circuit 18 in advance with a decoder 17, and either one set is selected at a switching circuit 19 in synchronizing with the high and low level of the series pattern data. With the pattern data converted into the serial data, a switching circuit 19 is controlled to decide which of color signal data (I=R, G, B) (J= color signal data at a latch circuit 18 at each 8-bit period of the serial pattern data and the preparation is executed in advance, then the circuit 19 performs mere selection (color signal data I or J). Thus, in the decoder 17, sufficient time margin is provided for the time decoding the color data from a color information memory 13, allowing to prevent the malfunction due to shortage of access time.

**(54) MAGNETIC RECORDER AND REPRODUCER**

- (11) 58-186289 (A) (43) 31.10.1983 (19) JP  
 (21) Appl. No. 57-67174 (22) 23.4.1982  
 (71) TOKYO SHIBAURA DENKI K.K. (72) SHIYUHEI KANDA  
 (51) Int. Cl.<sup>3</sup> H04N9/497, G11B5/52, H04N9/02, H04N9/491

**PURPOSE:** To record characters on a reproduced screen newly, by mixing an FM signal formed with an FM frequency oscillator and a low frequency conversion chrominance subcarrier signal having an equal frequency fluctuation to that of the low frequency conversion color signal in a reproduced video signal, during the detection of the



11: control section, 12: pattern memory, 13: color information memory, 14: output port

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